

TABLE 5.1
Unloaded BJT Transistor Amplifiers

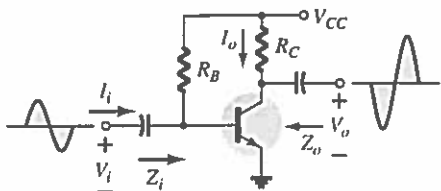
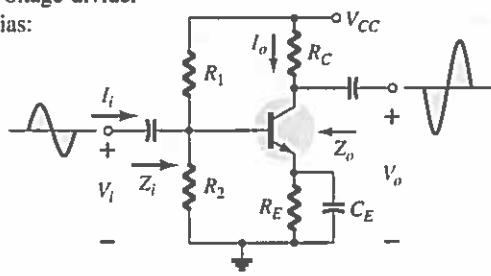
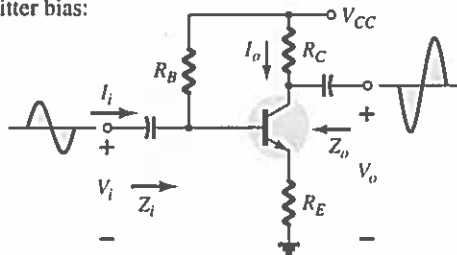
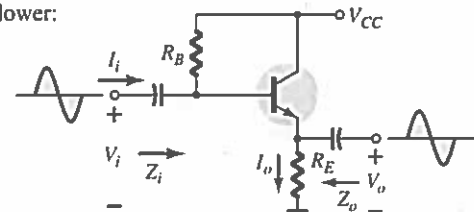
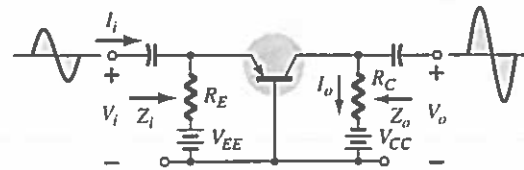
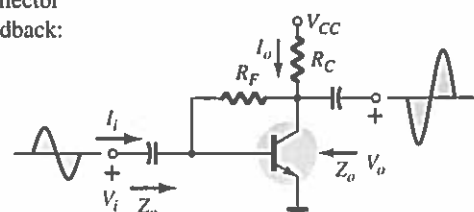
Configuration	Z_i	Z_o	A_v	A_i
Fixed-bias: 	Medium (1 kΩ) $= R_B \parallel \beta r_e$ $\approx \beta r_e$ $(R_B \geq 10\beta r_e)$	Medium (2 kΩ) $= R_C \parallel r_o$ $\approx R_C$ $(r_o \geq 10R_C)$	High (-200) $= \frac{(R_C \parallel r_o)}{r_e}$ $\approx \frac{R_C}{r_e}$ $(r_o \geq 10R_C)$	High (100) $= \frac{\beta R_B r_o}{(r_o + R_C)(R_B + \beta r_e)}$ $\approx \beta$ $(r_o \geq 10R_C, R_B \geq 10\beta r_e)$
Voltage-divider bias: 	Medium (1 kΩ) $= R_1 \parallel R_2 \parallel \beta r_e$	Medium (2 kΩ) $= R_C \parallel r_o$ $\approx R_C$ $(r_o \geq 10R_C)$	High (-200) $= \frac{R_C \parallel r_o}{r_e}$ $\approx \frac{R_C}{r_e}$ $(r_o \geq 10R_C)$	High (50) $= \frac{\beta(R_1 \parallel R_2)r_o}{(r_o + R_C)(R_1 \parallel R_2 + \beta r_e)}$ $\approx \frac{\beta(R_1 \parallel R_2)}{R_1 \parallel R_2 + \beta r_e}$ $(r_o \geq 10R_C)$
Unbypassed emitter bias: 	High (100 kΩ) $= R_B \parallel Z_b$ $Z_b \approx \beta(r_e + R_E)$ $\approx R_B \parallel \beta R_E$ $(R_E \gg r_e)$	Medium (2 kΩ) $= R_C$ (any level of r_o)	Low (-5) $= \frac{R_C}{r_e + R_E}$ $\approx \frac{R_C}{R_E}$ $(R_E \gg r_e)$	High (50) $\approx \frac{\beta R_B}{R_B + Z_b}$
Emitter-follower: 	High (100 kΩ) $= R_B \parallel Z_b$ $Z_b \approx \beta(r_e + R_E)$ $\approx R_B \parallel \beta R_E$ $(R_E \gg r_e)$	Low (20 Ω) $= R_E \parallel r_e$ $\approx r_e$ $(R_E \gg r_e)$	Low (≈ 1) $= \frac{R_E}{R_E + r_e}$ ≈ 1	High (-50) $\approx \frac{\beta R_B}{R_B + Z_b}$
Common-base: 	Low (20 Ω) $= R_E \parallel r_e$ $\approx r_e$ $(R_E \gg r_e)$	Medium (2 kΩ) $= R_C$	High (200) $\approx \frac{R_C}{r_e}$	Low (-1) ≈ -1
Collector feedback: 	Medium (1 kΩ) $= \frac{r_e}{\frac{1}{\beta} + \frac{R_C}{R_F}}$ $(r_o \geq 10R_C)$	Medium (2 kΩ) $\approx R_C \parallel R_F$ $(r_o \geq 10R_C)$	High (-200) $\approx \frac{R_C}{r_e}$ $(r_o \geq 10R_C, R_F \gg R_C)$	High (50) $= \frac{\beta R_F}{R_F + \beta R_C}$ $\approx \frac{R_F}{R_C}$

TABLE 5.2
BJT Transistor Amplifiers Including the Effect of R_s and R_L

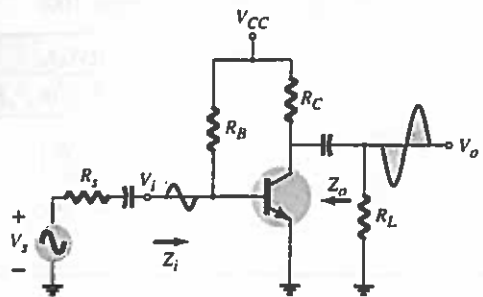
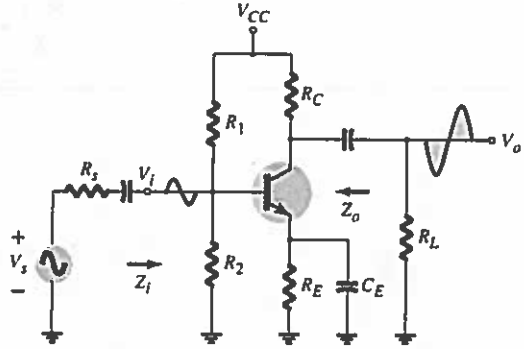
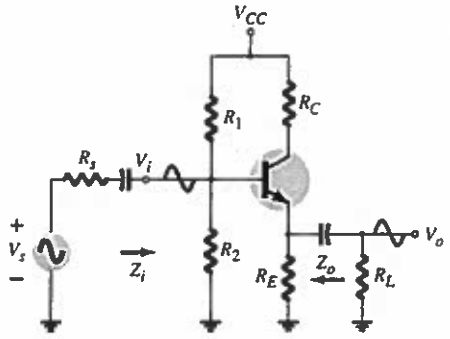
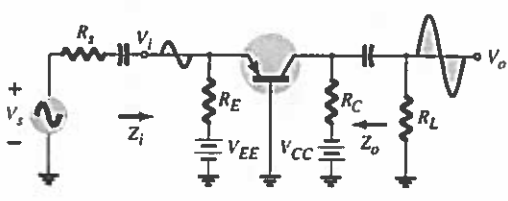
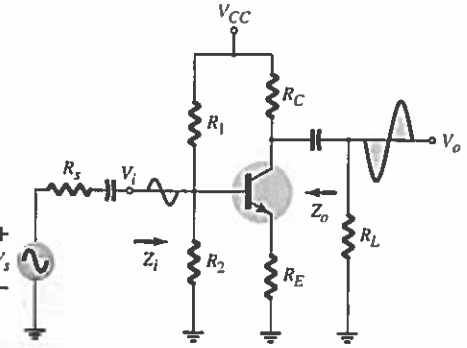
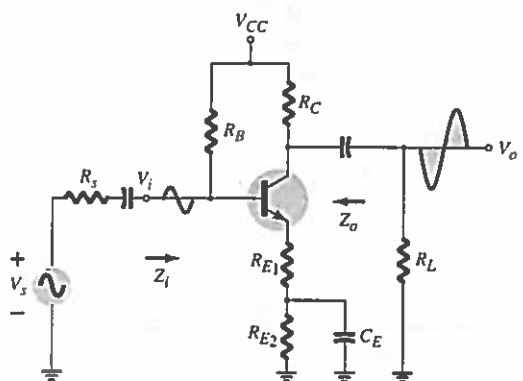
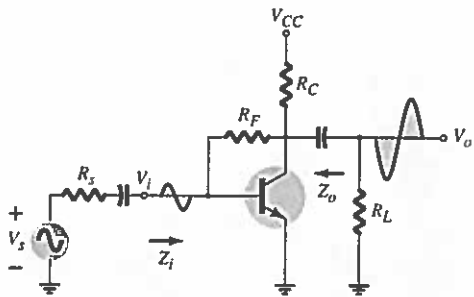
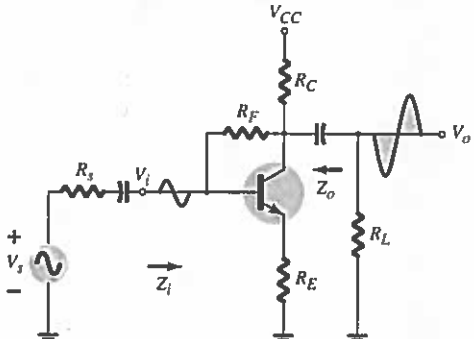
Configuration	$A_{v_L} = V_o/V_i$	Z_i	Z_o
	$\frac{-(R_L \parallel R_C)}{r_e}$	$R_B \parallel \beta r_e$	R_C
	Including r_o : $\frac{(R_L \parallel R_C \parallel r_o)}{r_e}$	$R_B \parallel \beta r_e$	$R_C \parallel r_o$
	$\frac{-(R_L \parallel R_C)}{r_e}$	$R_1 \parallel R_2 \parallel \beta r_e$	R_C
	Including r_o : $\frac{-(R_L \parallel R_C \parallel r_o)}{r_e}$	$R_1 \parallel R_2 \parallel \beta r_e$	$R_C \parallel r_o$
	≈ 1	$R'_E = R_L \parallel R_E$ $R_1 \parallel R_2 \parallel \beta(r_e + R'_E)$	$R'_s = R_s \parallel R_1 \parallel R_2$ $R_E \parallel \left(\frac{R'_s}{\beta} + r_e \right)$
	Including r_o : ≈ 1	$R_1 \parallel R_2 \parallel \beta(r_e + R'_E)$	$R_E \parallel \left(\frac{R'_s}{\beta} + r_e \right)$
	$\approx \frac{-(R_L \parallel R_C)}{r_e}$	$R_E \parallel r_e$	R_C
	Including r_o : $\approx \frac{-(R_L \parallel R_C \parallel r_o)}{r_e}$	$R_E \parallel r_e$	$R_C \parallel r_o$
	$\frac{-(R_L \parallel R_C)}{R_E}$	$R_1 \parallel R_2 \parallel \beta(r_e + R_E)$	R_C
	Including r_o : $\frac{-(R_L \parallel R_C)}{R_E}$	$R_1 \parallel R_2 \parallel \beta(r_e + R_e)$	$\approx R_C$

TABLE 5.2 (Continued)
BJT Transistor Amplifiers Including the Effect of R_s and R_L

Configuration	$A_{v_L} = V_o/V_i$	Z_i	Z_o
	$\frac{-(R_L \parallel R_C)}{R_{E1}}$	$R_B \parallel \beta(r_e + R_{E1})$	R_C
	Including r_o : $\frac{-(R_L \parallel R_C)}{R_{E1}}$	$R_B \parallel \beta(r_e + R_E)$	$\cong R_C$
	$\frac{-(R_L \parallel R_C)}{r_e}$	$\beta r_e \parallel \frac{R_F}{ A_v }$	R_C
	Including r_o : $\frac{-(R_L \parallel R_C \parallel r_o)}{r_e}$	$\beta r_e \parallel \frac{R_F}{ A_v }$	$R_C \parallel R_F \parallel r_o$
	$\frac{-(R_L \parallel R_C)}{R_E}$	$\beta R_E \parallel \frac{R_F}{ A_v }$	$\cong R_C \parallel R_F$
	Including r_o : $\cong \frac{-(R_L \parallel R_C)}{R_E}$	$\cong \beta R_E \parallel \frac{R_F}{ A_v }$	$\cong R_C \parallel R_F$

packaged system relates to the actual amplifier or network. The system of Fig. 5.61 is called a two-port system because there are two sets of terminals—one at the input and the other at the output. At this point it is particularly important to realize that

the data surrounding a packaged system is the no-load data.

This should be fairly obvious because the load has not been applied, nor does it come with the load attached to the package.

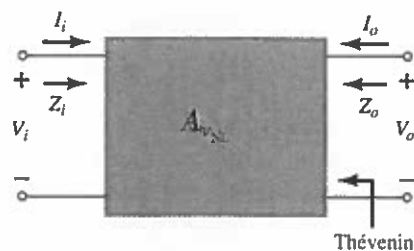


FIG. 5.61
Two-port system.

TABLE 8.1
 Z_i , Z_o , and A_v for various FET configurations

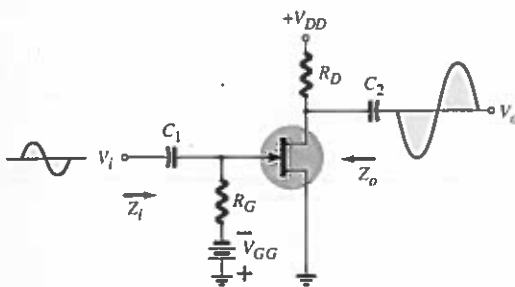
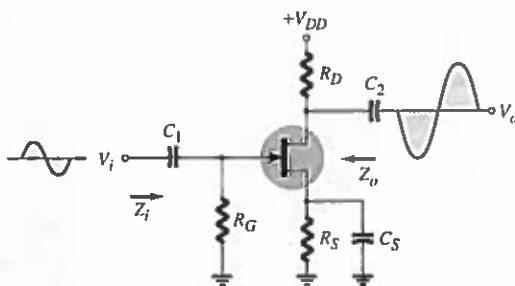
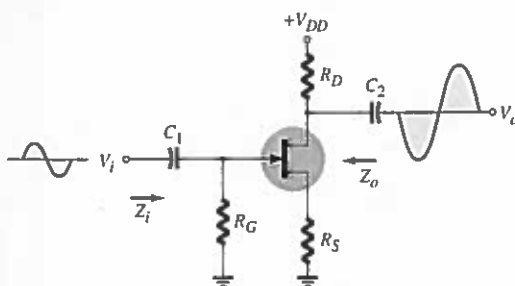
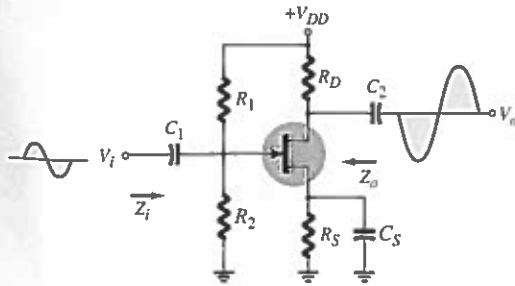
Configuration	Z_i	Z_o	$A_v = \frac{V_o}{V_i}$
Fixed-bias [JFET or D-MOSFET] 	High (10 MΩ) $= R_G$	Medium (2 kΩ) $= R_D \parallel r_d$ $\cong R_D \quad (r_d \geq 10 R_D)$	Medium (-10) $= -g_m(r_d \parallel R_D)$ $\cong -g_m R_D \quad (r_d \geq 10 R_D)$
Self-bias bypassed R_S [JFET or D-MOSFET] 	High (10 MΩ) $= R_G$	Medium (2 kΩ) $= R_D \parallel r_d$ $\cong R_D \quad (r_d \geq 10 R_D)$	Medium (-10) $= -g_m(r_d \parallel R_D)$ $\cong -g_m R_D \quad (r_d \geq 10 R_D)$
Self-bias unbypassed R_S [JFET or D-MOSFET] 	High (10 MΩ) $= R_G$	$= \frac{\left[1 + g_m R_S + \frac{R_S}{r_d}\right] R_D}{\left[1 + g_m R_S + \frac{R_S}{r_d} + \frac{R_D}{r_d}\right]}$ $= R_D \quad (r_d \geq 10 R_D \text{ or } r_d \cong \infty)$	Low (-2) $= \frac{g_m R_D}{1 + g_m R_S + \frac{R_D + R_S}{r_d}}$ $\cong \frac{g_m R_D}{1 + g_m R_S} \quad (r_d \geq 10(R_D + R_S))$
Voltage-divider bias [JFET or D-MOSFET] 	High (10 MΩ) $= R_1 \parallel R_2$	Medium (2 kΩ) $= R_D \parallel r_d$ $\cong R_D \quad (r_d \geq 10 R_D)$	Medium (-10) $= -g_m(r_d \parallel R_D)$ $\cong -g_m R_D \quad (r_d \geq 10 R_D)$

TABLE 8.1
(Continued)

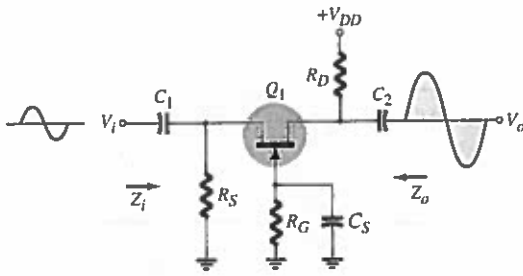
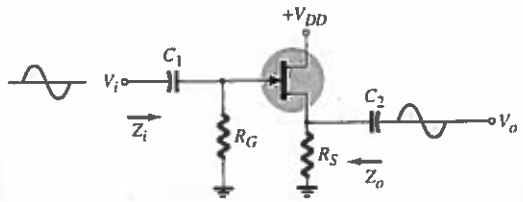
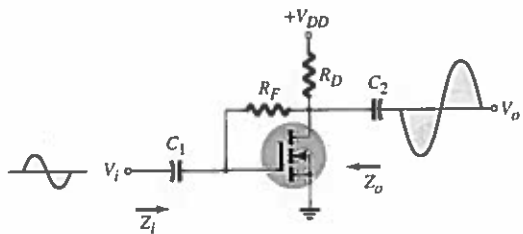
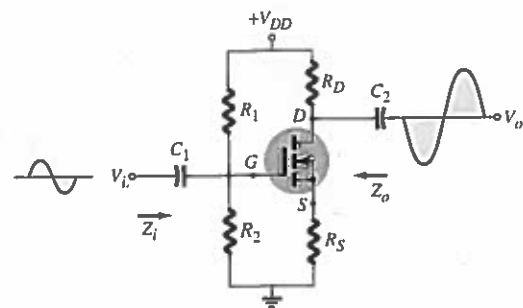
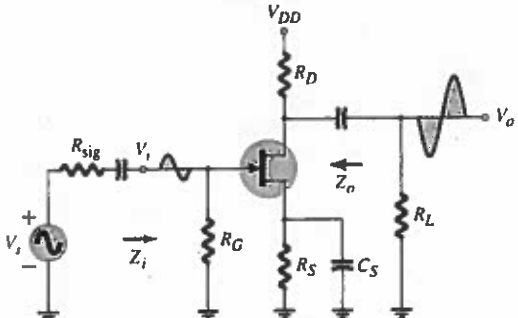
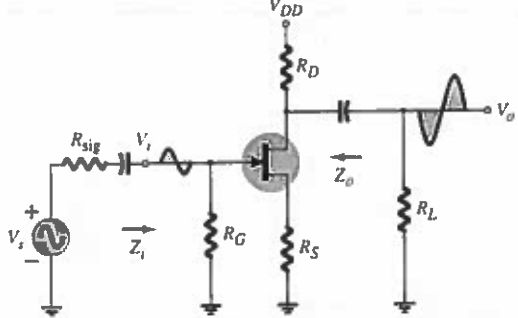
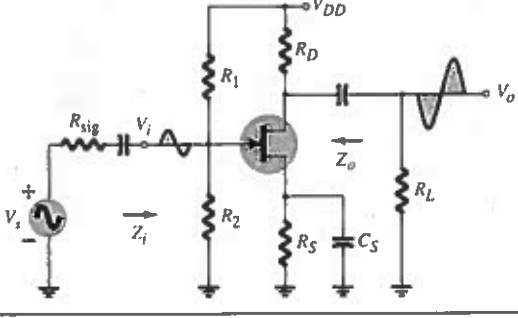
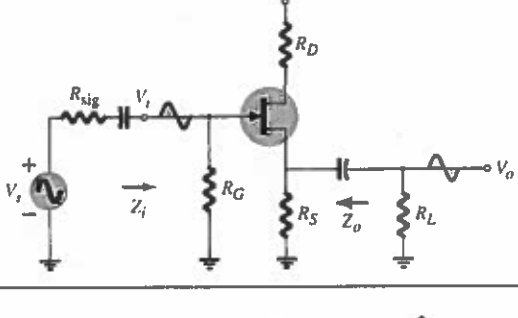
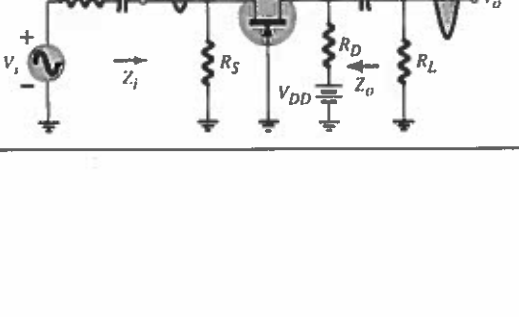
Configuration	Z_i	Z_o	$A_v = \frac{V_o}{V_i}$
Common-gate [JFET or D-MOSFET] 	Low (1 kΩ) $= R_S \parallel \left[\frac{r_d + R_D}{1 + g_m r_d} \right]$ $\approx R_S \parallel \frac{1}{g_m} \quad (r_d \approx 10 R_D)$	Medium (2 kΩ) $= R_D \parallel r_d$ $\approx R_D \quad (R_D \approx 10 R_D)$	Medium (+10) $= \frac{g_m R_D + \frac{R_D}{r_d}}{1 + \frac{R_D}{r_d}}$ $\approx g_m R_D \quad (r_d \approx 10 R_D)$
Source-follower [JFET or D-MOSFET] 	High (10 MΩ) $= R_G$	Low (100 kΩ) $= r_d \parallel R_S \parallel 1/g_m$ $\approx R_S \parallel 1/g_m \quad (r_d \approx 10 R_S)$	Low (<1) $= \frac{g_m (r_d \parallel R_S)}{1 + g_m (r_d \parallel R_S)}$ $\approx \frac{g_m R_S}{1 + g_m R_S} \quad (r_d \approx 10 R_S)$
Drain-feedback bias E-MOSFET 	Medium (1 MΩ) $= \frac{R_F + r_d \parallel R_D}{1 + g_m (r_d \parallel R_D)}$ $\approx \frac{R_F}{1 + g_m R_D} \quad (r_d \approx 10 R_D)$	Medium (2 kΩ) $= R_F \parallel r_d \parallel R_D$ $\approx R_D \quad (R_F, r_d \approx 10 R_D)$	Medium (-10) $= -g_m (R_F \parallel r_d \parallel R_D)$ $\approx -g_m R_D \quad (r_d, r_d \approx 10 R_D)$
Voltage-divider bias E-MOSFET 	Medium (1 MΩ) $= R_1 \parallel R_2$	Medium (2 kΩ) $= R_D \parallel r_d$ $\approx R_D \quad (r_d \approx 10 R_D)$	Medium (-10) $= -g_m (r_d \parallel R_D)$ $\approx -g_m R_D \quad (r_d \approx 10 R_D)$

TABLE 8.2

Configuration	$A_{v_L} = V_o \parallel V_i$	Z_i	Z_o
	$-g_m(R_D \parallel R_L)$ Including r_d : $-g_m(R_D \parallel R_L \parallel r_d)$	R_G R_G	R_D $R_D \parallel r_d$
	$\frac{-g_m(R_D \parallel R_L)}{1 + g_m R_S}$ Including r_d : $\frac{-g_m(R_D \parallel R_L)}{1 + g_m R_S + \frac{R_D + R_S}{r_d}}$	R_G R_G	$\frac{R_D}{1 + g_m R_S}$ $\cong \frac{R_D}{1 + g_m R_S}$
	$-g_m(R_D \parallel R_L)$ Including r_d : $-g_m(R_D \parallel R_L \parallel r_d)$	$R_1 \parallel R_2$ $R_1 \parallel R_2$	R_D $R_D \parallel r_d$
	$\frac{g_m(R_S \parallel R_L)}{1 + g_m(R_S \parallel R_L)}$ Including r_d : $= \frac{g_m r_d (R_S \parallel R_L)}{r_d + R_D + g_m r_d (R_S \parallel R_L)}$	R_G R_G	$R_S \parallel 1/g_m$ $\frac{R_S}{1 + \frac{g_m r_d R_S}{r_d + R_D}}$
	$g_m(R_D \parallel R_L)$ Including r_d : $\cong g_m(R_D \parallel R_L)$	$\frac{R_S}{1 + g_m R_S}$ $Z_i = \frac{R_S}{1 + \frac{g_m r_d R_S}{r_d + R_D \parallel R_L}}$	R_D $R_D \parallel r_d$